Abstract—In this paper presents a Design of a Unified Power Quality conditioner (UPQC) connected to three phase four wire system (3P4W). The neutral of series transformer used in the fourth wire for the 3P4W system. The neutral current that may flow toward transformer neutral point is compensated by using a four-leg voltage source inverter topology for shunt part. The series transformer neutral will be at virtual zero potential during all operating conditions. Here we observe the power quality problems such as unbalanced voltage and current, harmonics by connecting non linear load to 3P4W system with Unified Power Quality conditioner. A new control strategy is proposed to the control algorithm for series APF is based on unit vector template generation to compensate the current unbalance present in the load currents by expanding the concept of single phase P-Q theory. The P-Q theory applied for balanced three phase system. And also be used for each phase of unbalanced system independently. The MATLAB/Simulink based simulations are provided the functionality of the UPQC.

I. INTRODUCTION

The power electronic devices due to their inherent non-linearity draw harmonic and reactive power from the supply. In three phase systems, they could also cause unbalance and draw excessive neutral currents. The injected harmonics, reactive power burden, unbalance, and excessive neutral currents cause low system efficiency and poor power factor. The use of the sophisticated equipment/loads at transmission and distribution level has increased considerably in recent years due to the development in the semiconductor device technology. The equipment needs clean power in order to function properly. At the same time, the switching operation of these devices generates current harmonics resulting in a polluted distribution system. The power-electronics-based devices have been used to overcome the major power quality problems [1]. A 3P4W distribution system can be realized by providing the neutral conductor along with the 3 power lines from generation station. The unbalanced load currents are very common and an important Problem in 3P4W distribution system [2]. To improve the power quality by connecting the series active power filter (APF) and shunt (APF). They are two types of filters. One is passive filters and another one is active filters.

Manuscript received November 10, 2011; revised February 20, 2012.
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II. THE 3P3W DISTRIBUTION SYSTEM UTILIZING UPQC

Generally, a 3P4W distribution system is realized by providing a neutral conductor along with three power conductors from generation station. Fig.1 shows the 3P3W system is connected to UPQC.

If we want to upgrade the system now from 3P3W to 3P4W due to installation of some single-phase loads and if the distribution transformer is close to the plant under consideration, utility would provide the neutral conductor from this transformer without major cost involvement. In recent cases, this may be a costly solution because the...
distribution transformer may not be situated in close vicinity. Recently, the utility service providers are putting more and more restrictions on current total harmonic distortion (THD) limits, drawn by nonlinear loads, to control the power distribution system harmonics pollution. At the same time, the use of sophisticated equipment or load has increases significantly, and it needs clean power for its proper operation. Fig. 2 shows the novel 3P4W topology that can be realized from a 3P3W system. In addition to easy expansion of 3P3W system to 3P4W system.

As shown in Fig. 1 the UPQC should necessarily consist of three-phase series transformer in order to connect one of the inverters in the series with the line to function as a controlled voltage source. If we could use the neutral of three-phase series transformer to connect a neutral wire to realize the 3P4W system, then 3P4W system can easily be achieved from a 3P3W system (fig. 2). The neutral current, present if any, would flow through this fourth wire toward transformer neutral point. This neutral current can be compensated by using a split capacitor topology or a four leg voltage source inverter (VSI) topology for a shunt inverter [4], [5]. The four-leg VSI topology requires one additional leg as compared to the split capacitor topology. VSI structure is much easier than that of the split capacitor. But here going through the UPQC design by using P-Q theory and it is connected to 3P4W system.

Thus, the structure would help to realize a 3P4W system at distribution load end. This would eventually result in easy expansion from 3P3W to 3P4W systems. A new control strategy to generate balanced reference source currents under load condition is also proposed in this paper and also UPQC design by using P-Q theory is also explained in the next section.

III. DESIGN OF UPQC CONTROLLER

A. Description of Implementation of Series APF

In series APF the Inverter injects a voltage in series with the line which feeds the polluting load through a transformer. The injected voltage will be mostly harmonic with a small amount of sinusoidal component which is in-phase with the current flowing in the line. The small sinusoidal in-phase (with line current) component in the injected voltage results in the right amount of active power flow into the Inverter to compensate for the losses within the Series APF and to maintain the D.C side capacitor voltage constant. Obviously the D.C voltage control loop will decide the amount of this in-phase component. Series active power filter compensate current system distortion caused by nonlinear load by imposing a high impedance path to the harmonic current [6]. The line diagram of series active power filter is shown in below fig. 3.

B. Description of Implementation of Shunt APF

The active filter concept uses power electronics to produce harmonic current components that cancel the Harmonic current components from the non-linear loads. The active filter uses Power electronic switching to generate harmonic currents that cancel the harmonic currents from a non-linear load. In this configuration, the filter is connected in parallel with the load being compensated. Therefore the configuration is often referred to as an active parallel or shunt filter [7], [8].

Fig. 4 illustrates the concept of the harmonic current cancellation so that the current being supplied from the source is sinusoidal. The voltage source inverter used in the active filter makes the harmonic control possible. This inverter uses dc capacitors as the supply and can switch at a high frequency to generate a signal that will cancel the harmonics from the non-linear load.

The control algorithm for series APF is based on unit vector template generation scheme [9]. Where as the control strategy for shunt APF is discussed in this section. Based on the load on the 3P4W system, the current drawn from the utility can be unbalanced. In this paper, the concept of single phase P-Q theory [10]. According to this theory, a single
phase system can be defined as a pseudo two-phase system by giving \(\pi/2\) lead or \(\pi/2\) lag, that is each phase voltage and current of the original three phase systems. These resultant two phase systems can be represented in \(\alpha-\beta\) coordinates, and thus P-Q theory applied for balanced three phase system [11] can also be used for each phase of unbalanced system independently. In order to eliminate these limitations, the reference load voltage signals extracted for series APF are used instead of actual load voltage [12],[13].

For phase a, the load voltage in \(\alpha-\beta\) coordinates can be represented by \(\pi/2\) lead as

\[
\begin{bmatrix}
  v_{La,\alpha} \\
  v_{La,\beta}
\end{bmatrix} = \begin{bmatrix}
  v_{La}(o) \\
  v_{La}(o + \pi/2)
\end{bmatrix} = \begin{bmatrix}
  V_{Lm} \sin(o) \\
  V_{Lm} \cos(o)
\end{bmatrix}
\]

\[\begin{bmatrix}
  i_{La-\alpha} \\
  i_{La-\beta}
\end{bmatrix} = \begin{bmatrix}
  i_{La}(o + \varphi_L) \\
  i_{La}(o + \varphi_L + \pi/2)
\end{bmatrix}
\]  

(2)

where \(v_{La}(o)\) represents the reference load voltage and \(V_{Lm}\) represents the desired load voltage magnitude.

Similarly, for phase b, the load voltage in \(\alpha-\beta\) coordinates can be represented by \(\pi/2\) lead as

\[
\begin{bmatrix}
  v_{Lb,\alpha} \\
  v_{Lb,\beta}
\end{bmatrix} = \begin{bmatrix}
  v_{Lb}(o) \\
  v_{Lb}(o + \pi/2)
\end{bmatrix} = \begin{bmatrix}
  V_{Lm} \sin(o - 120') \\
  V_{Lm} \cos(o - 120')
\end{bmatrix}
\]

\[\begin{bmatrix}
  i_{Lb-\alpha} \\
  i_{Lb-\beta}
\end{bmatrix} = \begin{bmatrix}
  i_{Lb}(o + \varphi_L) \\
  i_{Lb}(o + \varphi_L + \pi/2)
\end{bmatrix}
\]

(3)

In addition, for phase c, the load voltage in \(\alpha-\beta\) coordinates can be represented by \(\pi/2\) lead as

\[
\begin{bmatrix}
  v_{Lc,\alpha} \\
  v_{Lc,\beta}
\end{bmatrix} = \begin{bmatrix}
  v_{Lc}(o) \\
  v_{Lc}(o + \pi/2)
\end{bmatrix} = \begin{bmatrix}
  V_{Lm} \sin(o + 120') \\
  V_{Lm} \cos(o + 120')
\end{bmatrix}
\]

\[\begin{bmatrix}
  i_{Lc-\alpha} \\
  i_{Lc-\beta}
\end{bmatrix} = \begin{bmatrix}
  i_{Lc}(o + \varphi_L) \\
  i_{Lc}(o + \varphi_L + \pi/2)
\end{bmatrix}
\]

(4)

By using the definition of three-phase system [2], the instantaneous power components can be represented as

Instantaneous active power

\[
P_{La} = \sqrt{v_{La-\alpha} \cdot i_{La-\alpha}} + \sqrt{v_{La-\beta} \cdot i_{La-\beta}}
\]

(5)

Instantaneous reactive power

\[
q_{La} = \sqrt{-v_{La-\alpha} \cdot i_{La-\beta}} + \sqrt{-v_{La-\beta} \cdot i_{La-\alpha}}
\]

(6)

Considering phase a, the phase- \(\alpha\) instantaneous load active and instantaneous load reactive powers can be represented by

\[
\begin{bmatrix}
  p_{La} \\
  q_{La}
\end{bmatrix} = \begin{bmatrix}
  v_{La-\alpha} + v_{La-\beta} \\
  -v_{La-\beta} + v_{La-\alpha}
\end{bmatrix} \begin{bmatrix}
  i_{La-\alpha} \\
  i_{La-\beta}
\end{bmatrix}
\]

\[\begin{bmatrix}
  p_{La} \\
  q_{La}
\end{bmatrix} = \begin{bmatrix}
  p_{La+} + p_{La-} \\
  q_{La+} + q_{La-}
\end{bmatrix}
\]

(7)

(8)

(9)

Therefore, the instantaneous fundamental load active power for phase a is given by

\[
p_{La1} = \overline{p_{La}}
\]

(10)

And the instantaneous fundamental load reactive power for phase a is given by

\[
q_{La1} = \overline{q_{La}}
\]

(11)

The instantaneous fundamental load active power for phase b is given by

\[
p_{Lb1} = \overline{p_{Lb}}
\]

(12)

The instantaneous fundamental load reactive power for phase b is given by

\[
q_{Lb1} = \overline{q_{Lb}}
\]

(13)

The instantaneous fundamental load active power for phase b is given by

\[
p_{Lc1} = \overline{p_{Lc}}
\]

(14)

The instantaneous fundamental load reactive power for phase b is given by

\[
q_{Lc1} = \overline{q_{Lc}}
\]

(15)

The aforementioned task can be achieved by summing instantaneous fundamental load active power demands of all the three phases and redistributing it again on each utility phase from (12), (14), (16)

\[
p_{Ltotal} = p_{La1} + p_{Lb1} + p_{Lc1}
\]

(16)

\[
p_{S/ph} = \frac{p_{Ltotal}}{3}
\]

(17)

Thus, the reference compensating currents are representing a perfectly balanced 3-phase system can be extracted by taking the inverse of (9)

\[
\begin{bmatrix}
  i_{La-\alpha}^{*} \\
  i_{La-\beta}^{*}
\end{bmatrix} = \begin{bmatrix}
  v_{La-\alpha} \\
  v_{La-\beta}
\end{bmatrix}^{-1} \cdot \begin{bmatrix}
  p_{S/ph} \\
  p_{dc/ph}
\end{bmatrix}
\]

(18)

In (20), \(p_{dc/ph}\) is the precise amount of per-phase active power that should be taken from the source in order to maintain the dc-link voltage at a constant level and to overcome the losses associated with UPQC.

Therefore, the reference source current for phase a, b and c can be estimated as

\[
i_{La}^{*}(t) = \frac{-v_{La-\beta}}{v_{La-\alpha} + v_{La-\beta}} \left( p_{S/ph}(t) + p_{dc/ph}(t) \right)
\]

(19)

\[
i_{Lb}(t) = \frac{-v_{Lb-\beta}}{v_{Lb-\alpha} + v_{Lb-\beta}} \left( p_{S/ph}(t) + p_{dc/ph}(t) \right)
\]

(20)

\[
i_{Lc}(t) = \frac{-v_{Lc-\beta}}{v_{Lc-\alpha} + v_{Lc-\beta}} \left( p_{S/ph}(t) + p_{dc/ph}(t) \right)
\]

(21)

The reference neutral current signal can be extracted by simply adding all the sensed load currents, without actual neutral current sensing, as

\[
i_{L-n}(t) = i_{la}(t) + i_{lb}(t) + i_{lc}(t)
\]

(22)

\[
i_{sh-n}(t) = -i_{L-n}(t)
\]

(23)

By using above equations to design the both series and shunt active power filters by connecting the 3P4W system as shown in next section.
IV. SIMULATION BLOCK DIAGRAM

The simulation block diagram of 3P4W system realized from a 3P3W system utilizing UPQC is shown in fig. 6. Non-linear loads means by connecting power electronics devices to system, by using universal bridge with R-L elements is connect to system. The plant load is assumed to be the combination of a balanced three-phase diode bridge rectifier followed by an R-L load, which acts as a harmonic generating load, and three different single phase loads on each phase, with different load active and reactive power demands. By using equations (1), (3) and (5) to design the unit vector template of series APF is shown in fig. 5 and fig. 7 is Series active power filter controller shown in below. And also shunt APF is design by using all above equations is shown in below fig. 8.

Fig. 6. Simulation block diagram of 3P4W system realized from a 3P3W system utilizing UPQC.

Fig. 5. Simulation block of Unit vector template of series active power filter.

Fig. 7. Simulation block of Series active power filter controller.

V. SIMULATION RESULTS AND DISCUSSION

The simulation results for the proposed 3P4W system realized from a 3P3W system utilizing UPQC are shown in below fig. 9 to 12. Utility voltage are assumed to be distorted with voltage THD of 14.03 %. The distorted voltage profile is shown in fig. 10 in utility voltage. The resulting load current profile shown in figure. 10 has THD of 12.10%. The UPQC should maintain the voltage at load bus at a desired value and free from distortion. The shunt APF is turned on first at time t=0.1sec, such that it maintains the dc-link voltage at a set reference value, here V=220V. At time t=0.2sec (is shown in fig. 6), the series active power filter injects the required compensating voltages through series transformer, making the load voltage free from distortion (THD = 1.46%) and at a desired level as shown in figure. 9 in load voltage. The series active power filter injected voltage profile is shown in fig. 9. The compensated source currents shown in fig. 10 are perfectly balanced with the THD of 2.26%. The compensating current injected through the fourth leg of the shunt APF is shown in fig. 10. The load neutral current profile is shown in fig. 11. In fig. 12, the shunt APF effectively compensates the current flowing toward the transformer neutral point. Thus, the series transformer neutral point is maintained at virtual zero potential.

Fig. 9. Utility voltage (\(v_{u,abc}\)) and load voltage (\(v_{L,abc}\)) and injected voltage (\(v_{inj,abc}\)).
The design of a unified power quality conditioner (UPQC) connected to 3P4W distribution system has been presented in this paper. Where upqc is installed to compensate the different power quality problems, which may play an important role in future upqc-based distribution system. The simulation results show that the distorted and unbalanced load currents seen from the utility side act as perfectly balanced source currents and are free from distortion. The series transformer neutral will be at virtual zero potential during all operating conditions. Here we can absorb the power quality problems like voltage and current unbalanced and finding the total harmonic distortion (THD) of 3P4W system utilizing 3P3W system to connect the UPQC.

VI. CONCLUSION

The design of a unified power quality conditioner (UPQC) connected to 3P4W distribution system has been presented in this paper. Where upqc is installed to compensate the different power quality problems, which may play an important role in future upqc-based distribution system. The simulation results show that the distorted and unbalanced load currents seen from the utility side act as perfectly balanced source currents and are free from distortion. The series transformer neutral will be at virtual zero potential during all operating conditions. Here we can absorb the power quality problems like voltage and current unbalanced and finding the total harmonic distortion (THD) of 3P4W system utilizing 3P3W system to connect the UPQC.