Embedded Controlled Three Level Inverter Based Dynamic Voltage Restorer (DVR)

S. Leela and S.S. Dash

Abstract—This paper deals with simulation and implementation of three level inverter based Dynamic Voltage Restorer(DVR). The control of DVR that injects a voltage in series with a distribution feeder is presented. DVR is a power electronic controller that can protect sensitive loads from disturbances in supply system. DVR can regulate the voltage at the load. Laboratory model is developed and the experimental results are compared with simulation studies.

Index Terms—Dynamic Voltage Restorer(DVR), Matlab simulink, Series Compensation, Three level inverter.

I. INTRODUCTION

A power electronic converter based series compensator that can protect critical loads from all supply side disturbances other than outages is called a dynamic voltage restorer. The restorer is capable of generating or absorbing independently controllable real and reactive power at its AC output terminal. This device employs solid state power electronic switches in a pulse width modulated (PWM) inverter structure. It injects a set of three phase AC output voltages in series and synchronism with the distribution feeder voltages. The amplitude and phase angle of the injected voltages are variable there by allowing control of the real and reactive power exchange between the device and the distribution system. The DC input terminal of the restorer is connected to an energy source or an energy storage device of appropriate capacity. The reactive power exchanged between the restorer and the distribution system is internally generated by the restorer without AC passive reactive components. The real power exchanged at the restorer output AC terminals is provided by the restorer input DC terminal from an external energy source or energy storage system.

In August 1996, Westinghouse Electric Corporation installed world's first dynamic voltage restorer in Duke Power Company's 12.47 kV substation in Anderson, South Carolina. This was installed to provide protection to an automated rug manufacturing plant. Prior to this connection, the restorer was first installed at the Waltz Mill test facility near Pittsburgh for the full power tests. The test results are discussed in [1]. The next commissioning of the

restorer was done at Westinghouse in February 1997 in Powercor's 22 kV distribution system at Stanhope, Victoria, Australia to protect a dairy milk processing plant. The saving that results from the installation of this installation is estimated at over \$100,000 per year [2]. In the next phase of development, Westinghouse (now taken over by Siemens) installed world's first platform mounted dynamic voltage restorer to protect Northern Lights Community College and several other smaller loads in Dawson Creek, British Columbia, Canada [3].

This paper extends the concept of dynamic voltage restorer further to tightly regulate the load voltage. It can also perform the primary functions of the restorer, i.e., to protect the load from temporary voltage interruption, sag/swell etc. This device is called a dynamic voltage regulator (DVR). For the control operation of DVR we stipulate that real power supplied by the device in steady state is zero. Based on this stipulation, the references of the voltages that are to be injected in series are generated. This requires online extraction of the fundamental positive sequence based on the sampled values. Once the reference voltage's are obtained, they are tracked by the VSIs realizing the DVR.

The actual implementation of the DVR using inverters raises additional issues of switch frequency injection. To eliminate these, two different filter structures are discussed here. The inclusion of the filters further complicates the problem of voltage tracking. To facilitate proper voltage tracking a switching band control scheme is used here. The proposed DVR is validated through digital computer simulation studies. New approach to load balancing and power factor correction is given by Ghosh[4]. Power inverter operations for series compensation is given by [5]. Transient analysis of alternator is given by [6].

The above literature does not deal with the embedded implementation of DVR system using matlab simulink. In this work, an attempt is made to implement the DVR system using an embedded controller. Three level inverter is proposed to reduce the harmonic distortion.

II. DVR CHARACTERISTICS

In this section we shall present the fundamental, positive-sequence, steady state analysis of a DVR connected power system. The voltage regulation scheme is shown in Fig. 1. This consists the following:

- DVR: represented by voltage sources V_{fa} , V_{fb} and V_{fc}
- Supply voltage: represented by sources V_{sa} , V_{sb} and V_{sc}

The DVR is connected between a terminal bus on the left and a load bus on the right. The voltage sources are

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connected to the DVR terminals by a feeder with an impedance of $R+jX$. We shall assume that the loads are balanced and the load impedance is given by $Z_L = R_L + jX_L$. It is to be noted that the phase angle Φ_L between the load terminal V_L and the line current i_s depends on the load impedance and is independent of the line impedance or the DVR voltage

Fig. 1. Schematic diagram of a dynamic voltage restorer connected to a power system.

The objective of the discussion presented below is to regulate the magnitude of the load voltage equal to that of the source voltage through DVR voltage injection. Further we stipulate the following condition on the DVR:

- The DVR does not supply any real power in the steady state. This implies that the phase angle difference between DVR voltage phasor and line current phasor must be $\pi/2$ in the steady state.

Let us assume that the load current lags the load voltage. To draw a phasor diagram of the steady state operation, we assume that the load voltage is fixed at V per unit and the source voltage is allowed to vary. Since the primary target is to make the magnitudes of V_L and V_s equal, the locus of desirable V_s is the arc NB as shown in Fig. 2.

Fig. 2. Phasor diagram showing multiple solutions

To make the magnitude of the load voltage equal to that of the source voltage, the RI_s drop must be less than NM . If the drop is less than this limiting value, the DVR must compensate the entire reactive drop in the feeder and provide additional injection such that the source voltage becomes V per unit. It can be seen from Fig. 2 that there are two possible intersection points with the arc-one at A and the other at B. This implies that two possible values of DVR voltage and can be obtained for each feeder drop. For the first value, the source voltage will be along OA , while for the other value, it will along OB . It is needless to say that the best choice is the A intersection requiring much smaller voltage injection from the DVR.

III. SIMULATION RESULTS

Digital simulation is done using the blocks of Matlab simulink and the results are presented here. Three level inverter system is shown in Fig. 3a. Selective harmonic elimination is implemented by using phase shifting method. The output of the inverter without filter is shown in Fig. 3b. The output with filter is shown in Fig. 3c. DVR using multilevel inverter is shown in Fig. 4a. Scopes are connected to measure the inverter voltage and load voltage. The output of the inverter is injected into line through a transformer. At $t=0.2s$, the additional load is switched on. The voltage across load-1 decreases as shown in Fig. 4b. DVR injects the voltage at $t=0.4s$. The voltage across the load-1 and voltage across load-2 will resume to the normal value. FFT analysis for the output voltage is shown in Fig. 4c. The THD value is 27.3%.

Fig. 3a. Three Level Inverter.

Fig. 3b. Output Of Three Level Inverter Without Filter.

Fig. 3c. Output Of Three Level Inverter With Filter.

Fig. 4a. DVR Without LC Filter.

Fig. 4b. Injected Voltage, Voltage Across Load-1, Voltage Across Load-2.

Fig. 5c. Spectrum Of Output Voltage.

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Fig. 5a. DVR With LC Filter.

DVR system with LC filter is shown in Fig. 5a. Injected voltage, voltage across load-1 and voltage across load-2 are shown in Fig. 5b. The FFT analysis for the output voltage is shown in Fig. 5c. THD reduces to 0.8%. Thus the harmonics are reduced from 27.3% to 0.8% by using phase shifting method.

Fig 5b. Injected Voltage, Voltage Across Load-1, Voltage Across Load-2.

IV. EXPERIMENTAL RESULTS

Laboratory model of DVR system is fabricated and tested in the laboratory. The driving pulses required by the inverter are produced by using Atmel microcontroller. These pulses are amplified using driver amplifier. The hardware layout is shown in Fig. 6a. AC input voltage is shown in Fig. 6b. Driving pulses applied to the inverter are shown in Fig. 6c. The output of DVR system without filter is shown in Fig. 6d. The output of DVR system with filter is shown in Fig. 6e. It can be seen that the output is nearly sinusoidal.

Fig. 6a. Hardware Implementation.

Fig. 6b. AC Input Voltage.

Fig. 6c. Driving Pulses Of MOSFETS.

Fig. 6d. Output Of DVR Without Filter.

Fig. 6e. Output Of DVR With LC Filter.

V. CONCLUSION

This paper presents simulation and implementation results of DVR using cascaded three level inverter. The pulses given to the inverter-II are shifted by 36° with respect to the pulses given to the inverter-I to reduce the harmonics. This reduces the harmonics to 0.8%. Thus this paper demonstrates the capability of DVR to improve the voltage quality. Two different DVR structures are studied and the corresponding results are presented. It is better to use a filter in the output of inverter to reduce the heating.

The simulation is based on the assumption of balanced load and single phase circuit model. The experimental results are similar to the simulation results.

REFERENCES

- [1] N. H. Woodley, L. Morgan and A. Sundram, "Experience with an inverter-base dynamic voltage restorer," *IEEE Trans. Power Delivery*, vol. 14, no. 3, pp.1181-1185, 1999.
- [2] N. H. Woodley, A. Sundaram, B. Coulter and D. Morris, "Dynamic voltage restorer demonstration project experience," *12th Conf. Electric Power Supply Industry(CEPSI)*, Pattaya, Thailand, 1998.
- [3] N. H. Woodley, K. S. Berton, C. W. Edwards, B. Coulter, B. Ward, T. Einarson and A. Sundaram, "Platform-mounted DVR demonstrated project experience," *5th International Transmission and Distribution Conf. Distribution 2000*, Brisbane, 1999.
- [4] A. Ghosh and A. Joshi, "A new approach to load balancing and power factor correction in power distribution system," *IEEE Trans. on Power Delivery*, vol. 15, no. 1, pp.417-422, 2000.
- [5] C. G. Hochgraf, "Power inverter apparatus using a transformer with its primary winding connected to the source end and a secondary winding connected to load end of an ac power line to insert series compensation," *United States Patent*, no. US005905367A, May 1998.
- [6] W. A. Lyon, *Transient Analysis of Alternating-Current Machinery*, Chapter 2, John Wiley, New York, 1954.
- [7] J. D. Irwin, *Basic Engineering Circuit Analysis*, 4th Edition, Mac-milan, New York, 1993.

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